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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10083592	FILING DATE 02/27/2002	CLASS 363 323	SUBCLASS 284	GAU 2838	EXAMINER Vu
**APPLICANTS: Inatomi Koichi; Nagai Ryuta;					
BEST AVAILABLE COPY					
**CONTINUING DATA VERIFIED:					
** FOREIGN APPLICATIONS VERIFIED: JAPAN 2001-060569 03/05/2001					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO 108075-00078	
Verified and Acknowledged Examiners's initials					
TITLE : Control circuit of DC-DC converter					
<small>U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)</small>					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for 0.6
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg. Print Fig.
		Application Examiner	
<input type="checkbox"/> TERMINAL		PREPARED FOR ISSUE	
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